

ABSTRACT OF THE DISCLOSURE

The integrated circuit comprises: a shared memory connected to a bus; an audio/multiplex/de-multiplex processor accessing the shared memory via the bus; a video processor performing heavy processes accessing the shared memory via the bus; and a local memory accessed by the video processor without passing through the bus. Avoiding latency time caused by access contention, probability that the integrated circuit can complete processes to be done in real time increases. Image data is displayed on the display device smoothly without deterioration of quality of display.